

WEST Search History

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DATE: Thursday, January 06, 2005

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	<i>DB=JPAB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L12	(memory or RAM or DRAM) and (switch\$ or alternat\$) adj7 second clock	3
	<i>DB=USPT; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L11	L10 and (switch\$ or alternat\$) adj7 second clock	45
<input type="checkbox"/>	L10	L7 and (switch\$ or alternat\$) adj7 first clock	113
<input type="checkbox"/>	L9	L8 and (read\$ or writ\$ or access\$) adj4 (RAM or DRAM or memory)	147
<input type="checkbox"/>	L8	L7 and (switch\$ or alternat\$) adj6 (first clock or second clock)	169
<input type="checkbox"/>	L7	L6 and clock signal	184
<input type="checkbox"/>	L6	L5 and (switch\$ or alternat\$) adj7 (first clock or second clock)	226
<input type="checkbox"/>	L5	L4 and memory	157773
<input type="checkbox"/>	L4	RAM or DRAM	214512
	<i>DB=DWPI; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L3	l1 and arbitrat\$	0
	<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L2	l1 and arbitrat\$	13
<input type="checkbox"/>	L1	(memory and (DRAM or RAM) and clock and (alternat\$ or switch\$) adj3 clock\$ and read\$ and writ\$ and clock signal and synchroniz\$ adj4 clock\$ and (select\$ or alternat\$ or switch\$) adj5 clock\$ adj5 (first or second))	116

END OF SEARCH HISTORY

VIET Q. NGUYEN
PRIMARY EXAMINER